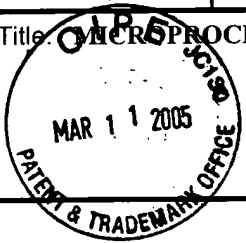


TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT (Under 37 CFR 1.97(b) or 1.97(c))				Docket No. 5035-202US	
In Re Application Of: Richard Michael Taylor					
Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/518,974	Dec. 21, 2004		20802		
Title: MICROPROCESSOR INSTRUCTION EXECUTION METHOD FOR EXPLOITING PARALLELISM					
<p>Address to:</p> <p>Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450</p> <p>37 CFR 1.97(b)</p> <p>1. <input checked="" type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.</p> <p style="text-align: center;">37 CFR 1.97(c)</p> <p>2. <input type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:</p> <p style="margin-left: 40px;"><input type="checkbox"/> the statement specified in 37 CFR 1.97(e);</p> <p style="text-align: center; margin-left: 100px;">OR</p> <p style="margin-left: 40px;"><input type="checkbox"/> the fee set forth in 37 CFR 1.17(p).</p>					



TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.
5035-202US

In Re Application: **Richard Michael Taylor**

Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/518,974	Dec. 21, 2004		20802		

Title: **MICROPROCESSOR INSTRUCTION EXECUTION METHOD FOR EXPLOITING PARALLELISM**

Payment of Fee

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- ☐ A check in the amount of _____ is attached.
- ☒ The Director is hereby authorized to charge and credit Deposit Account No. **23-3040** as described below.
- ☐ Charge the amount of _____
- ☐ Credit any overpayment.
- ☒ Charge any additional fee required.
- ☐ Payment by credit card. Form PTO-2038 is attached.

WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.

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I certify that this document and authorization to charge deposit account is being facsimile transmitted to the United States Patent and Trademark Office (Fax. No. _____)
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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on <u>March 9, 2005</u> (Date)
<u>Richard C. Woodbridge</u> Signature of Person Mailing Correspondence
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*This certificate may only be used if paying by deposit account.

Richard C. Woodbridge
Signature

Richard C. Woodbridge, Reg. No. 26,423
Synnestvedt Lechner & Woodbridge LLP
P.O. Box 592
Princeton, NJ 08542
609-924-3773 phone
609-924-1811 fax

Dated: March 9, 2005

cc: Origin

INFORMATION DISCLOSURE CITATION*(Use several sheets if necessary)*

ATTY DOCKET NO.

5035-202

APPLICATION NO.

10/518,974

APPLICANT(S)

Richard Michael Taylor

FILING DATE

Dec. 21, 2004

GROUP ART UNIT

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

U.S. PATENT APPLICATION PUBLICATIONS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	A	WO 03/040916 A1	15 May 2003	PCT				
	B	EP 081 0519 A2	30 May 1997	EP				
	C	EP 1 050 811 A1	03 May 1999					

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

	D	Hideki Ando, Chikako Nakanishi, Tetsuya Hara, Masao Nakaya; "Unconstrained Speculative Execution with Predicated State Buffering"; System LSI Laboratory; Mitsubishi Electric Corporation; 22nd ISCA, pp 126-137, June 1993
	E	Dionisios N. Pnevmatikatos; Gurinder S. Sohi; "Guarded Execution and Branch Prediction in Dynamic ILP Processors"; Computer Sciences Department; University of Wisconsin-Madison; pp 120-129

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>	ATTY DOCKET NO. 5035-2027	APPLICATION NO. 10/518,974
	Richard Michael Taylor	
	FILING Dec. 21, 2004	GROUP ART

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

U.S. PATENT APPLICATION PUBLICATIONS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

	F	Marc Tremblay; Jeffrey Chan; Shailender Chaudhry; Andrew W. Conigliaro; Shing Sheung Tse; "The MAJC Architecture: A Synthesis of Parallelism and Scalability"; November - December, IEEE Micro; pp 12 - 25
	G	Jenn-Yuan Tsai; Pen-Chung Yew; "Enhancing Multiple-Path Speculative Execution with Predicate Window Shifting"; pp 0 - 27

EXAMINER	DATE CONSIDERED
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.